

**IN THE SPECIFICATION**

Please replace the paragraph starting on page 1, line 3 with the following paragraph:

This application claims the benefit of U.S. Provisional Application No. 60/218,574 ~~\_\_\_\_\_ (LSI Docket No. 00-413/1P)~~, filed July 17, 2000, and is hereby incorporated by reference in its entirety.

Please replace the paragraph starting on page 1, line 8 with the following paragraph:

The present invention may relate to co-pending applications U.S. Serial No. 09/822,041 ~~09/\_\_\_\_\_ (LSI Docket No. 00-413)~~ and U.S. Serial No. 09/822,112 ~~09/\_\_\_\_\_ (LSI Docket No. 01-121)~~, filed concurrently, which are each hereby incorporated by reference in their entirety.

Please replace the paragraph starting on page 73, line 1 with the following paragraph:

An apparatus comprising an analog circuit, a first digital circuit, and a second digital circuit. The analog circuit may be configured to generate a plurality of samples of an input signal in response to a plurality of phases of a reference clock. The first digital circuit may be configured to generate (i) one or more data signals ~~signal~~, (ii) a first strobe signal, and (iii) a second strobe signal in response to the plurality of samples, the plurality of phases, and a correction signal. The second digital

circuit may be configured to generate the correction signal and a width signal in response to (i) the one or more data signals ~~signal~~, (ii) the first strobe signal, and (iii) the second strobe signal.